

Appl. No.: unassigned
Preliminary amendment dated Feb. 2, 2004

Amendments to the Specification:

On page 1 of the specification, please replace the first paragraph with the following paragraph:

The present invention is a continuation of Patent Application Serial Number 09/726,642, filed on November 30, 2000, which is a continuation of 09/439,121, filed November 12, 1999, which is a continuation-in-part application of the following applications filed on August 09, 1999, commonly owned by the assignee of the present application, the contents of each of which are herein incorporated by reference: Serial Number 09/370,353 entitled "Multi-Pair Transceiver Decoder System with Low Computation Slicer"; Serial Number 09/370,354 entitled "System and Method for High-Speed Decoding and ISI Compensation in a Multi-Pair Transceiver System"; Serial Number 09/370,370 entitled "System and Method for Trellis Decoding in a Multi-Pair Transceiver System"; and Serial Number 09/370,491 entitled "High-Speed Decoder for a Multi-Pair Gigabit Transceiver".